

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: C. OKAMOTO et al. Confirmation No.: 9276  
Serial No.: 10/687,753  
Filed: October 20, 2003  
For: METHOD OF MOUNTING ELECTRONIC CIRCUIT CHIP  
Group: 2841  
Examiner: A. Getachew

**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR §1.97 & 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

September 27, 2007

Sir:

In the matter of the above-identified application, Applicants are submitting herewith a copy of a communication from a foreign patent office in a counterpart foreign application and a form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted after the mailing date of a final action but before payment of the issue fee and is accompanied by the fee of \$180.00 set forth in 37 CFR §1.17(p) and the following certification specified in 37 CFR §1.97(e):

On information and belief, I hereby certify that each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

Each of the documents listed on the attached form equivalent to Form PTO-1449 is in the English language.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Mattingly, Stanger, Malur & Brundidge, Deposit Account No. 50-1417 (referencing attorney docket no. ASA-1003-02) please credit any excess fees to such deposit account.

Respectfully submitted,

MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C.

/Daniel J. Stanger/  
Daniel J. Stanger  
Registration No. 32,846

DJS/sdb  
(703) 684-1120